

TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

TD62783AP, TD62783AFW, TD62784AP, TD62784AFW (Manufactured by Toshiba Malaysia)

8CH HIGH-VOLTAGE SOURCE DRIVER

The TD62783AP / AFW Series are comprised of eight source current Transistor Array.

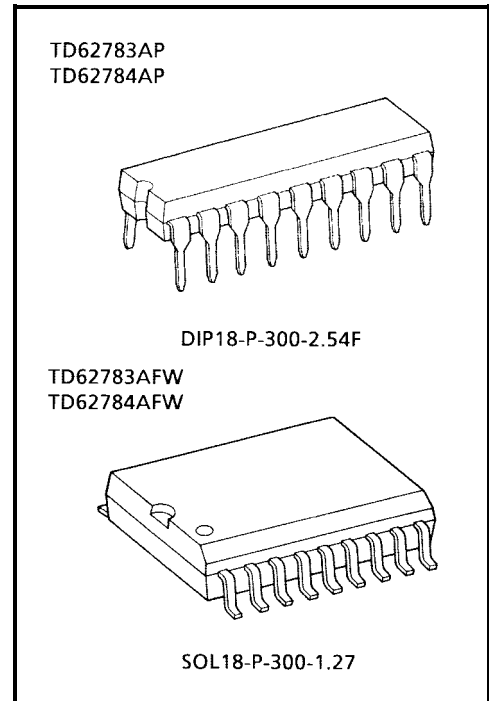
These drivers are specifically designed for fluorescent display applications.

Applications include relay, hammer and lamp drivers.

FEATURES

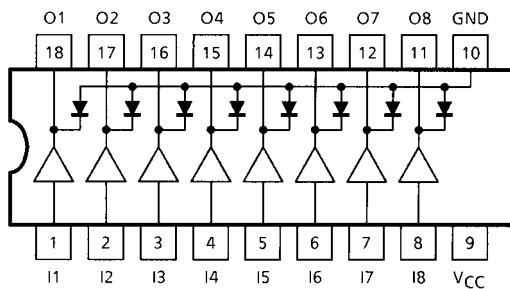
- High output voltage Type-AP, AFW : $V_{CC} = 50 \text{ V MIN.}$
 Type-F : $V_{CC} = 35 \text{ V MIN.}$
- Output current (single output) $I_{OUT} = -500 \text{ mA MIN.}$
- Output clamp diodes
- Single supply voltage
- Input compatible with various types of logic
- Package Type-AP : DIP-18 pin
- Package Type-AFW: SOL-18 pin

TYPE	DESIGNATION
TD62783AP / AFW	TTL, 5 V CMOS
TD62784AP / AFW	6~15 V PMOS, CMOS

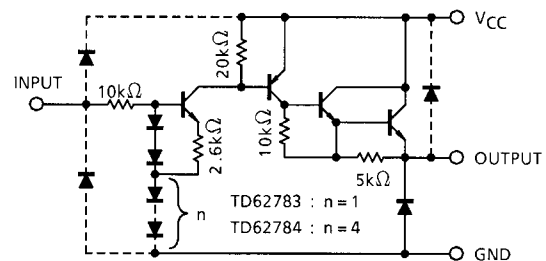


Weight
 DIP18-P-300-2.54F : 1.478 g (Typ.)
 SOL18-P-300-1.27 : 0.48 g (Typ.)

PIN CONNECTION (TOP VIEW)



SCHEMATICS (EACH DRIVER)



Note: The input and output parasitic diodes cannot be used as clamp diodes.

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT
Supply Voltage		V _{CC}	50	V
Output Current		I _{OUT}	-500	mA / ch
Input Voltage		V _{IN} (Note 1)	15	V
		V _{IN} (Note 2)	30	
Clamp Diode Reverse Voltage		V _R	50	V
Clamp Diode Forward Current		I _F	500	mA
Power Dissipation	AP	P _D	1.47	W
	AFW		0.92 / 1.31 (Note 3)	
Operating Temperature		T _{opr}	-40~85	°C
Storage Temperature		T _{stg}	-55~150	°C

Note 1: Only TD62783AP / AFW

Note 2: Only TD62784AP / AFW

Note 3: On Glass Epoxy PCB (75 × 114 × 1.6 mm Cu 20%)

RECOMMENDED OPERATING CONDITIONS (Ta = -40~85°C)

CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT	
Supply Voltage		V _{CC}	—	—	—	50	V	
Output Current		I _{OUT}	Ta = 85°C T _j = 120°C T _{pw} = 25ms	Duty = 10% 8Circuits	—	—	-260	mA / ch
				Duty = 50% 8Circuits	—	—	-59	
				Duty = 10% 8Circuits	—	—	-180	
				Duty = 50% 8Circuits	—	—	-38	
Input Voltage		V _{IN}	TD62783AP / AFW	—	—	12	V	
			TD62784AP / AFW	—	—	24		
Input Voltage	Output On	V _{IN} (ON)	TD62783AP / AFW	—	2.0	5.0	V	
			TD62784AP / AFW	—	4.5	12.0		30
	Output Off	V _{IN} (OFF)	TD62783AP / AFW	—	0	—		0.8
			TD62784AP / AFW	—	0	—		2.0
Clamp Diode Reverse Voltage		V _R	AP	—	—	50	V	
			AFW	—	—	35		
Clamp Diode Forward Current		I _F	—	—	—	400	mA	
Power Dissipation		P _D	Ta = 85°C	—	—	0.76	W	
			Ta = 85°C (Note)	—	—	0.48		

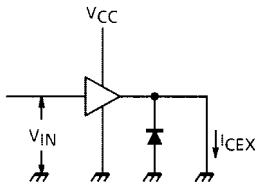
Note: On Glass Epoxy PCB (75 × 114 × 1.6 mm Cu 20%)

ELECTRICAL CHARACTERISTICS (Ta = 25°C)

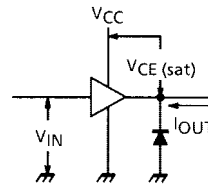
CHARACTERISTIC		SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT
Output Leakage Current		I_{CEX}	1	$V_{CC} = V_{CC \text{ MAX.}}$, $V_{IN} = 0.4 \text{ V}$ $T_a = 25^\circ\text{C}$	—	—	100	μA
Output Saturation Voltage		$V_{CE \text{ (sat)}}$	2	$V_{IN} = V_{IN \text{ (ON)}}$, $I_{OUT} = -350 \text{ mA}$	—	—	2.0	V
				$V_{IN} = V_{IN \text{ (ON)}}$, $I_{OUT} = -225 \text{ mA}$	—	—	1.9	
				$V_{IN} = V_{IN \text{ (ON)}}$, $I_{OUT} = -100 \text{ mA}$	—	—	1.8	
Input Current	TD62783AP / AFW	$I_{IN \text{ (ON)}}$	3	$V_{IN} = 2.4 \text{ V}$	—	36	52	μA
	TD62784AP / AFW			$V_{IN} = 3.85 \text{ V}$	—	180	260	
				$V_{IN} = 5 \text{ V}$	—	92	130	
				$V_{IN} = 12 \text{ V}$	—	790	1130	
Input Voltage	TD62783AP / AFW	$V_{IN \text{ (ON)}}$	4	$V_{CE} = 2.0 \text{ V}$	—	—	2.0	V
	TD62784AP / AFW			$I_{OUT} = -350 \text{ mA}$	—	—	4.5	
	TD62783AP / AFW	$V_{IN \text{ (OFF)}}$		$I_{OUT} = -500 \mu\text{A}$	0.8	—	—	
	TD62784AP / AFW			2.0	—	—		
Supply Current		$I_{CC \text{ (ON)}}$	3	$V_{IN} = V_{IN \text{ (ON)}}$, $V_{CC} = 50 \text{ V}$	—	—	2.5	mA / ch
Clamp Diode Reverse Current		I_R	5	$V_R = 50 \text{ V}$	—	—	50	μA
Clamp Diode Forward Voltage		V_F	6	$I_F = 350 \text{ mA}$	—	—	2.0	V
Turn-On Delay		t_{ON}	7	$V_{CC} = V_{CC \text{ MAX.}}$, $R_L = 125 \Omega$ $C_L = 15 \text{ pF}$, $R_L = 88 \Omega \text{ (F)}$	—	0.15	—	μs
Turn-Off Delay		t_{OFF}			—	1.8	—	

TEST CIRCUIT

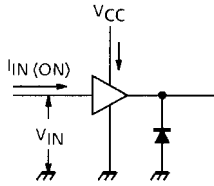
1. I_{CEX}



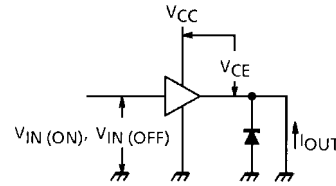
2. $V_{CE(sat)}$



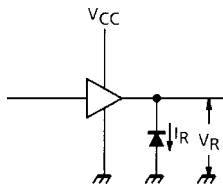
3. $I_{IN(ON)}, I_{CC}$



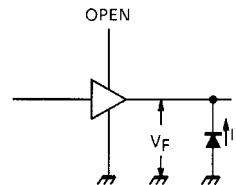
4. $V_{IN(ON)}, V_{IN(OFF)}$



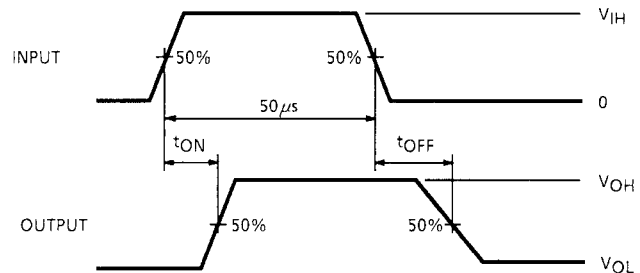
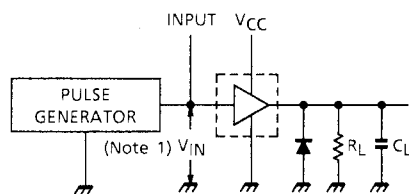
5. I_R



6. V_F



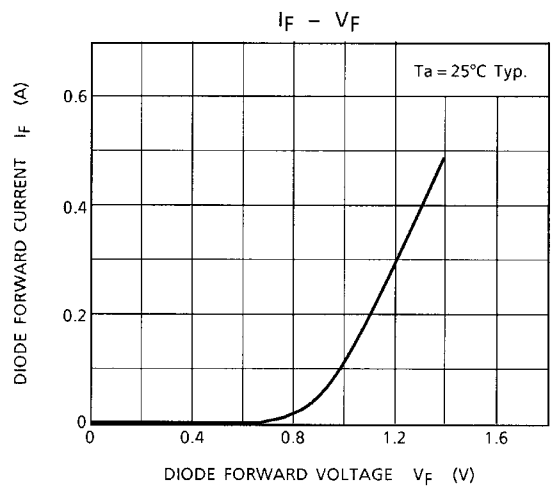
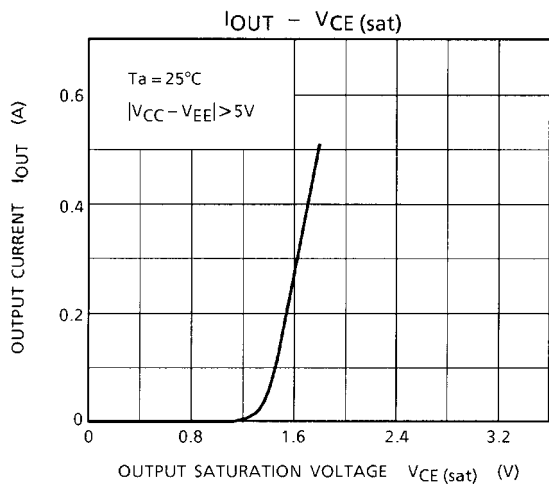
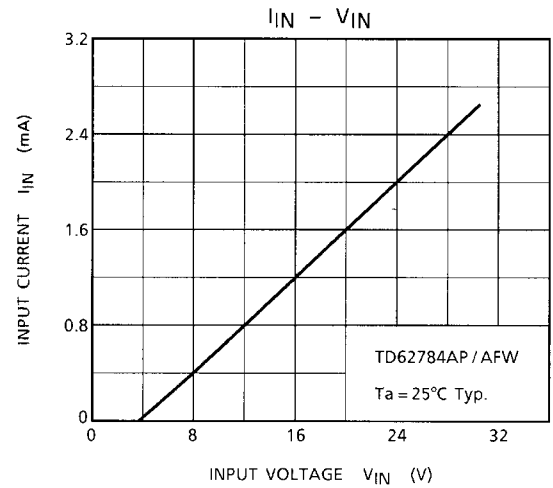
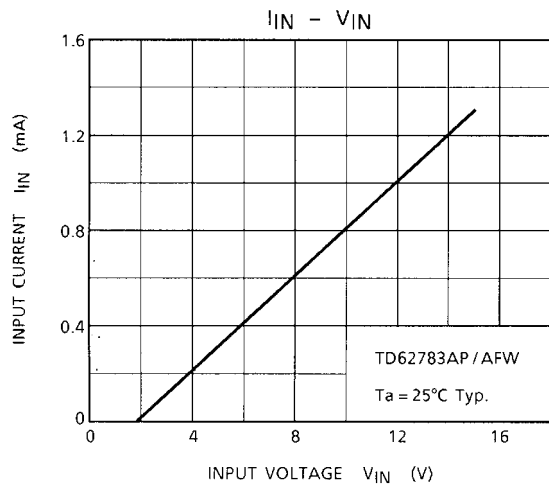
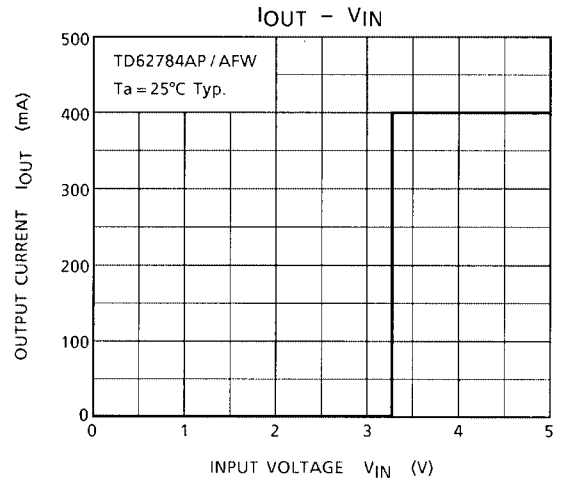
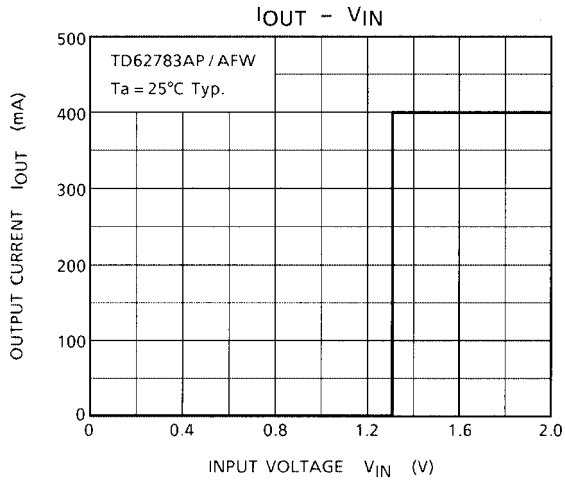
7. t_{ON}, t_{OFF}

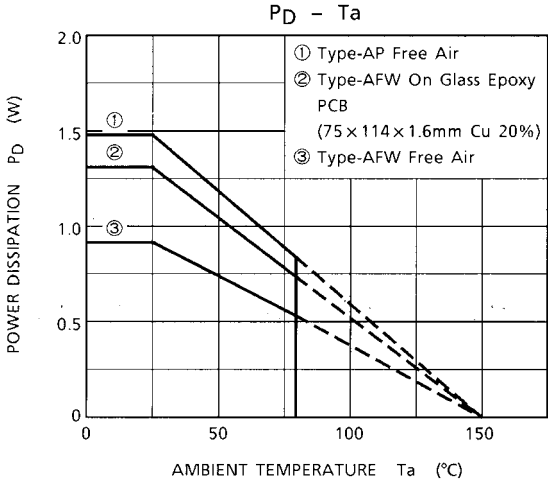


Note 1: Pulse width 50 μ s, duty cycle 10%
 Output impedance 50 Ω , $t_r \leq 5$ ns, $t_f \leq 10$ ns
 Note 2: C_L includes probe and jig capacitance

PRECAUTIONS for USING

This IC does not integrate protection circuits such as overcurrent and overvoltage protectors. Thus, if excess current or voltage is applied to the IC, the IC may be damaged. Please design the IC so that excess current or voltage will not be applied to the IC. Utmost care is necessary in the design of the output line, VCC and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.

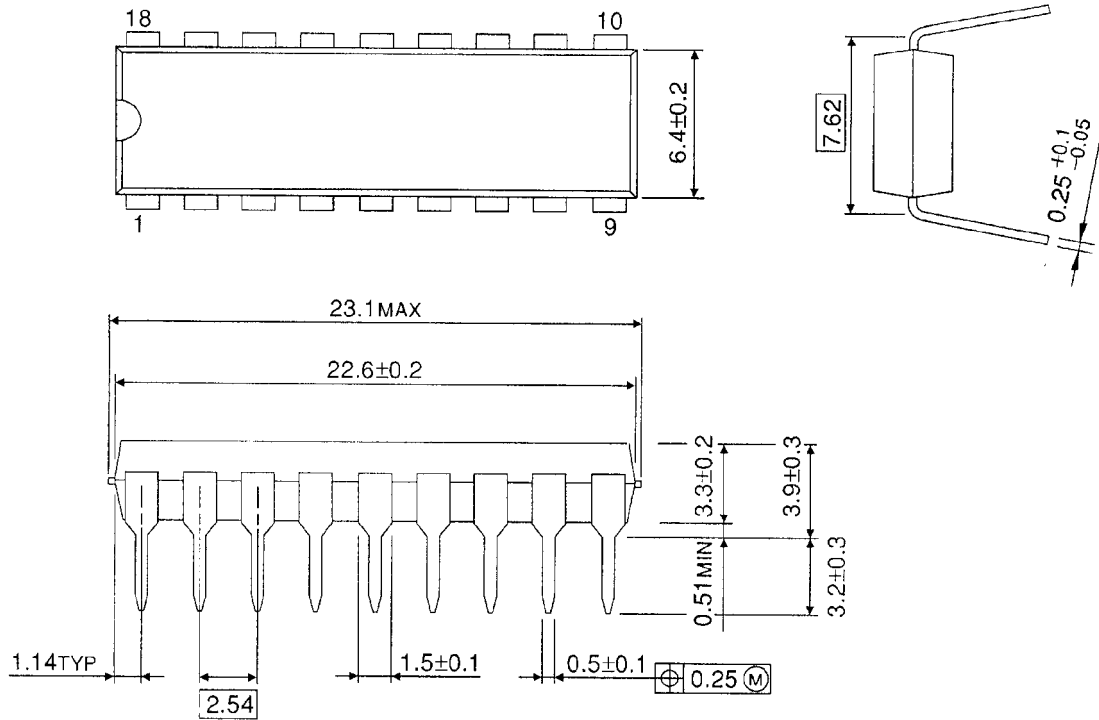




PACKAGE DIMENSIONS

DIP18-P-300-2.54F

Unit: mm

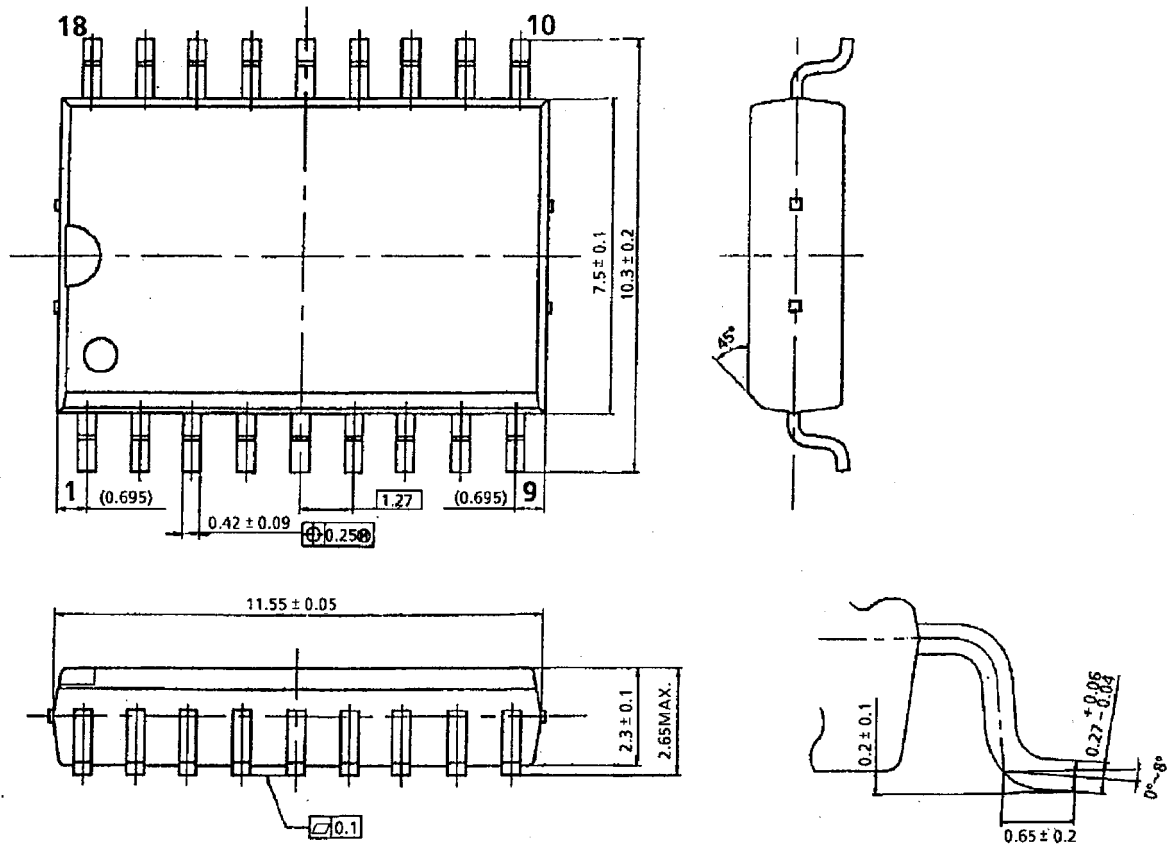


Weight: 1.478 g (Typ.)

PACKAGE DIMENSIONS

SOL18-P-300-1.27

Unit: mm



Weight: 0.48 g (Typ.)

RESTRICTIONS ON PRODUCT USE

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